Mymensingh Polytechnic Intitute
Electromedical Technology
Outline Plan of Teching 4th Semester Student
SEMESTER PLAN PRESENTATION   T   P   C
Subject: Digital Electronics (26845)   3   3   4

WEEK	Theory - Content (Specific Objective)	Activity - Class Test	Activity - Learning Material	Practical - Content (Practical Job no)	Activity
1	Understand the CONCEPT OF DIGITAL ELECTRONICS (1.1-1.6)		Ref. books, White board, marker.	Job no: 1 VERIFY THE TRUTH TABLES OF LOGIC GATES (OR, AND, NOT, NAND & NOR)	
2	Understand the Feature of NUMBER SYSTEM AND BINARY ARITHMETIC OPERATION (2.1-2.4)			Job no: 2 VERIFY THE TRUTH TABLE OF X-OR & X-NOR GATE USING BASIC GATES.	
3	Understand the Feature of CODES AND CODE CONVERSION (3.1-3.5)			Job no: 3 PERFORM THE OPERATION OF NAND & NOR GATE AS UNIVERSAL GATES.	
4	Understand the Feature of LOGIC GATES (4.1-4.3)	CT-1		Job no: 4 DESIGN & DEVELOP CODE CONVERTER CIRCUITS AND OBSERVE ITS OUTPUT.	
5	Understand the LOGIC GATES (4.4-4.6)			Job no: 5 OBSERVE THE FUNCTIONS OF HALF ADDER & SUBTRACTOR.	
6	Understand the Feature of ELECTRONIC CIRCUIT OF LOGIC GATES (5.1-5.4)	QT-1		Job no: 6 VERIFY THE FUNCTIONS OF FULL ADDER & FULL SUBTRACTOR.	
7	Understand the Feature of DIGITAL IC'S (6.1-6.4)			Job no: 7 VERIFY THE OUTPUT OPERATION OF BINARY 4 BIT PARALLEL ADDER.	
8	Understand the Feature of LOGIC FAMILIES (7.1-7.7)			Job no: 8 SHOW THE OPERATION OF ENCODER & DECODER.	
9	**MIDTERM EXAMINATION**				
10	Understand the Feature of LOGIC SIMPLIFICATION & CIRCUIT DESIGN (8.1-8.9)			Job no: 9 SHOW THE OPERATION OF 7 SEGMENT DISPLAY DRIVER.	
11	Understand the Feature of COMBINATIONAL LOGIC CIRCUITS (9.1-9.5)			Job no: 10 SHOW THE OPERATION OF MULTIPLEXER & DEMULTIPLEXER.	
12	Understand the Feature of ENCODER (10.1-10.5)	CT-2		Job no: 11 VERIFY THE TRUTH TABLE OF DIFFERENT S-R & D FLIP-FLOPS.	
13	Understand the Feature of DECODER (11.1-11.3)			Job no: 12 VERIFY THE TRUTH TABLE OF DIFFERENT J-K FLIP-FLOPS.	
14	Understand the Feature of MULTIPLEXERS (12.1-12.3)	QT-2		Job no: 13 VERIFY THE TRUTH TABLE OF DIFFERENT J-K FLIP-FLOPS.	
15	Understand the Feature of DEMULTIPLEXER. 13.1-13.4)			Job no: 14 VERIFY THE OPERATION OF BINARY COMPARATOR.	
16	Understand the Feature of SEQUENTIAL LOGIC CIRCUITS (14.1-14.4)			Job no: 14 VERIFY THE OPERATION OF BINARY COMPARATOR.	

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